

FIG. 4 - SYSTEM ARCHITECTURE

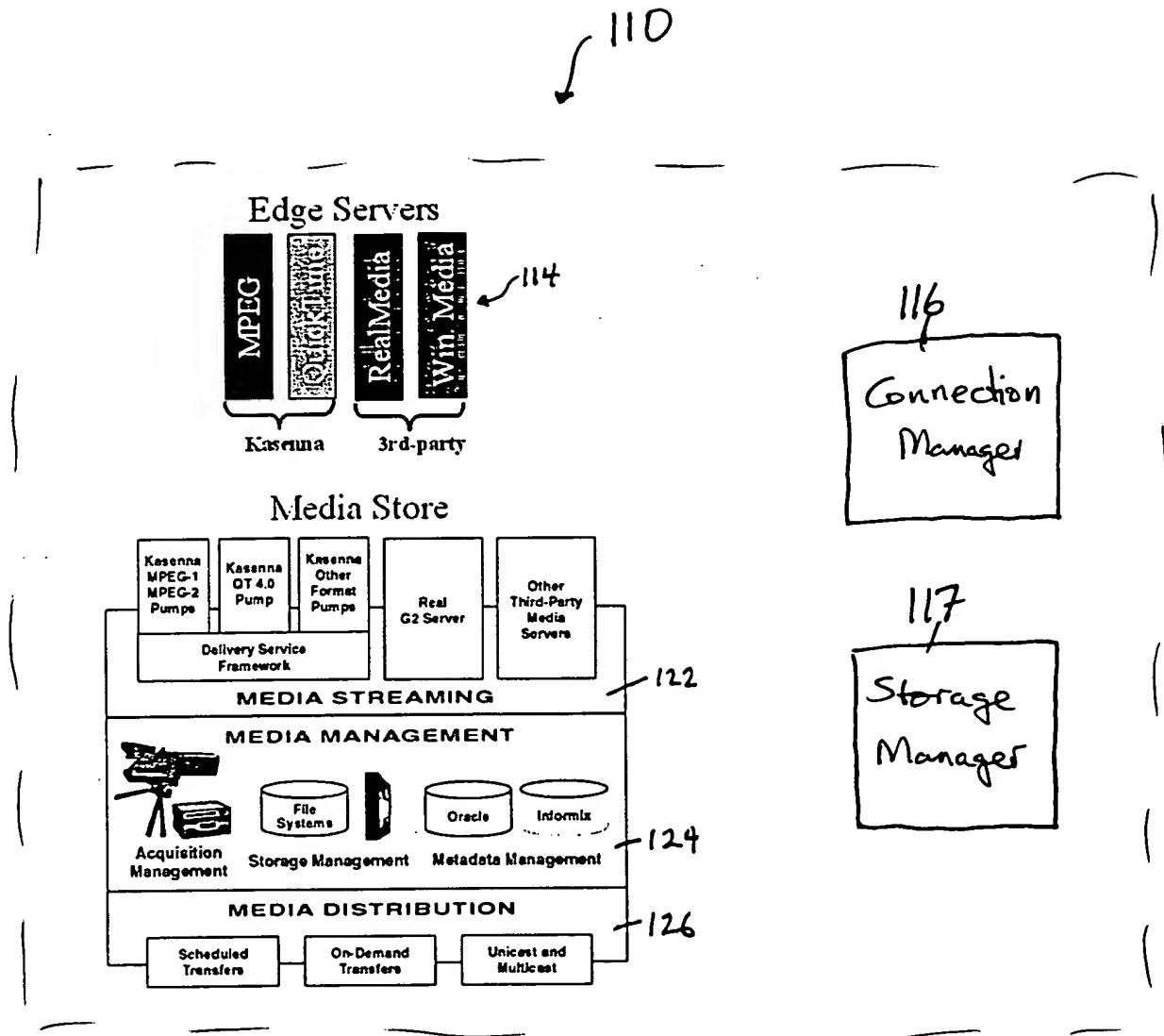


FIG. 4

FIG. 5

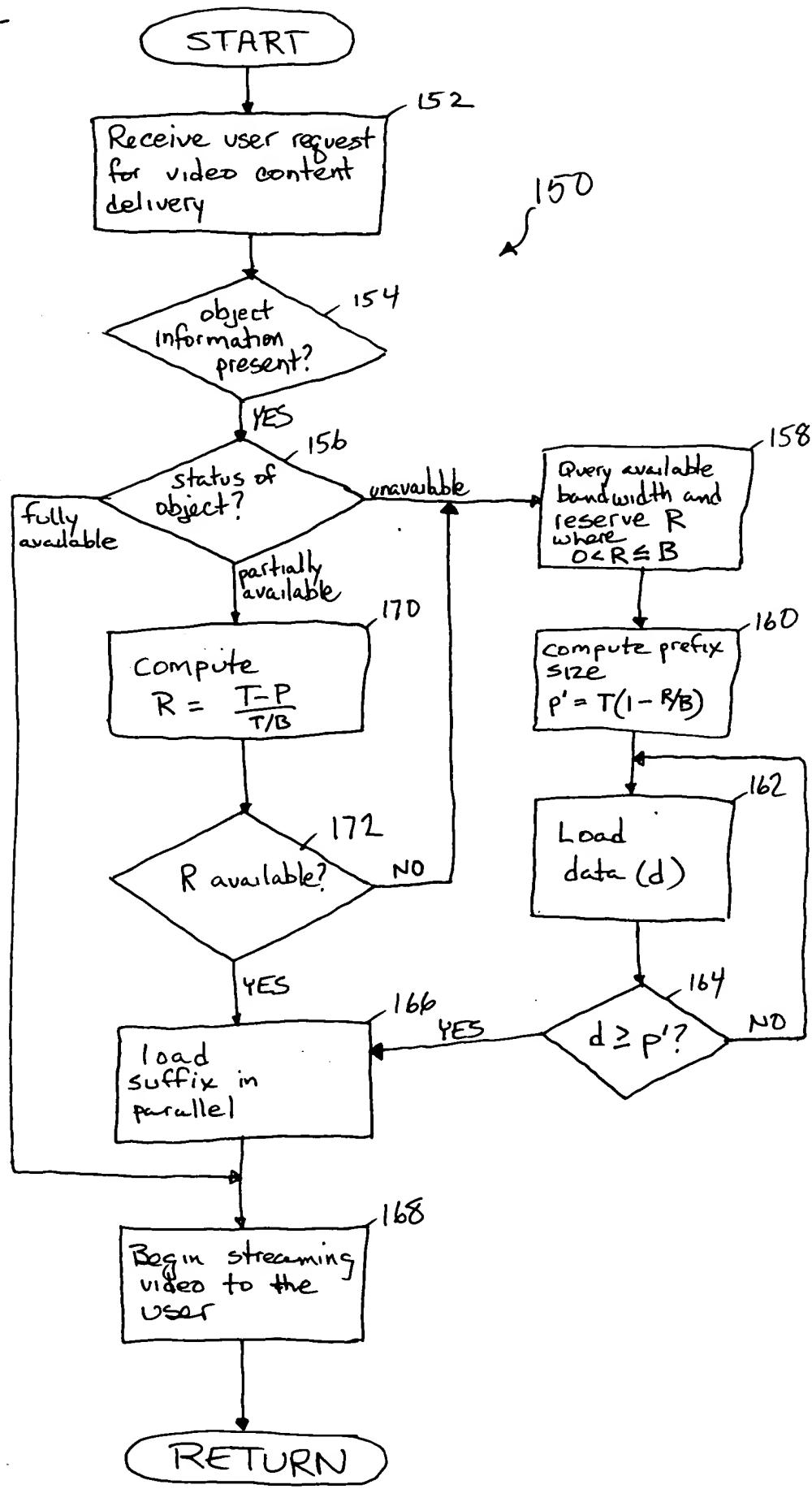


FIG. 6

